



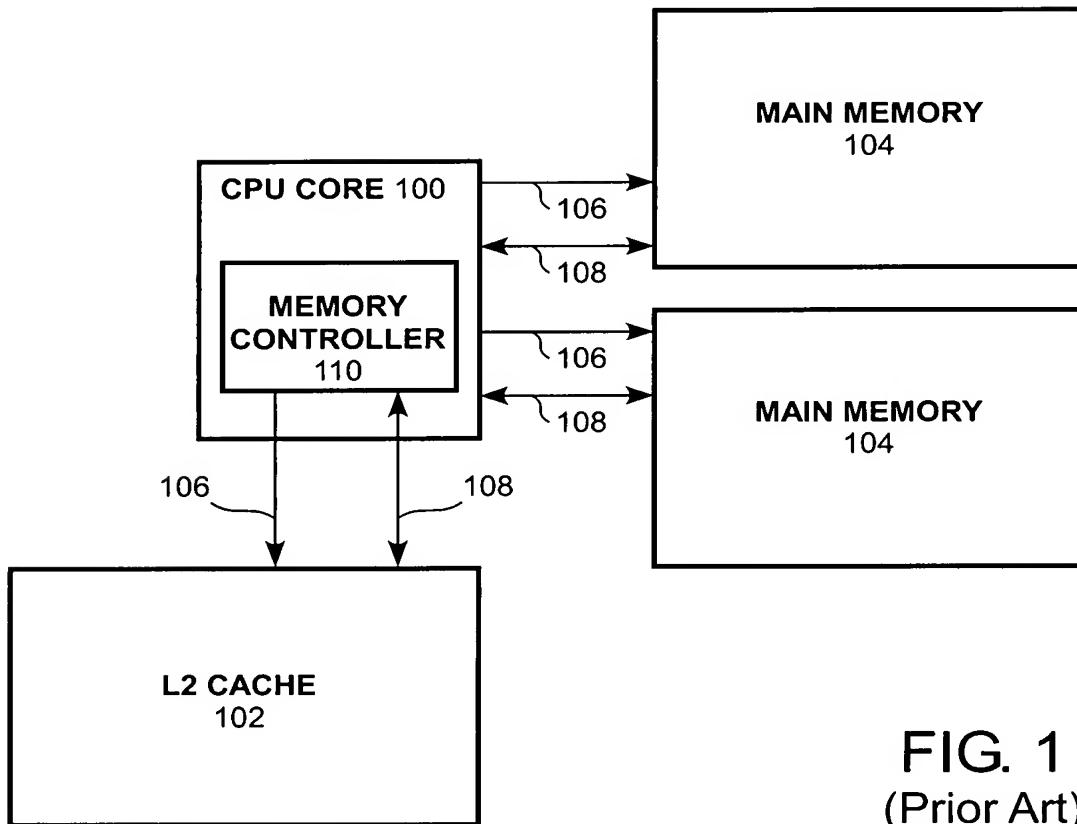
Title: Methods and Systems for Detecting Memory Address Transfer Errors in an Address Bus

Inventors: Wong et al.

REPLACEMENT SHEET

Application No: 10/766,743

Docket No: SUNMP382



**FIG. 1**  
(Prior Art)

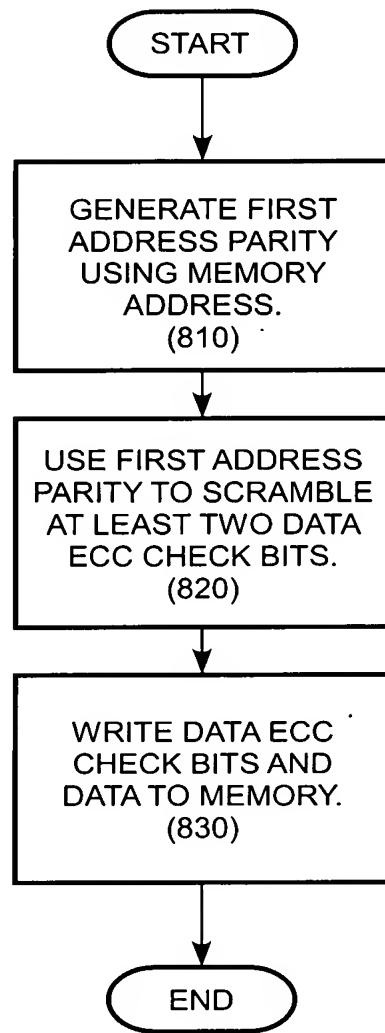


FIG. 2

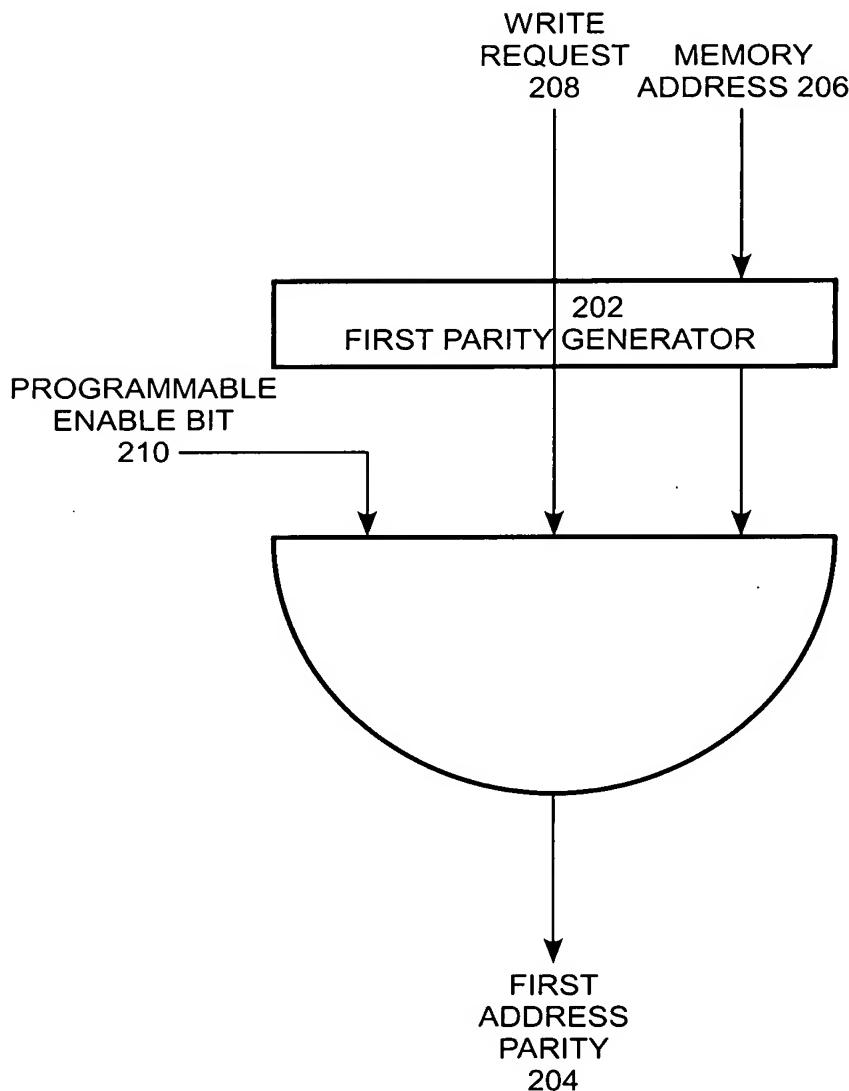


FIG. 3

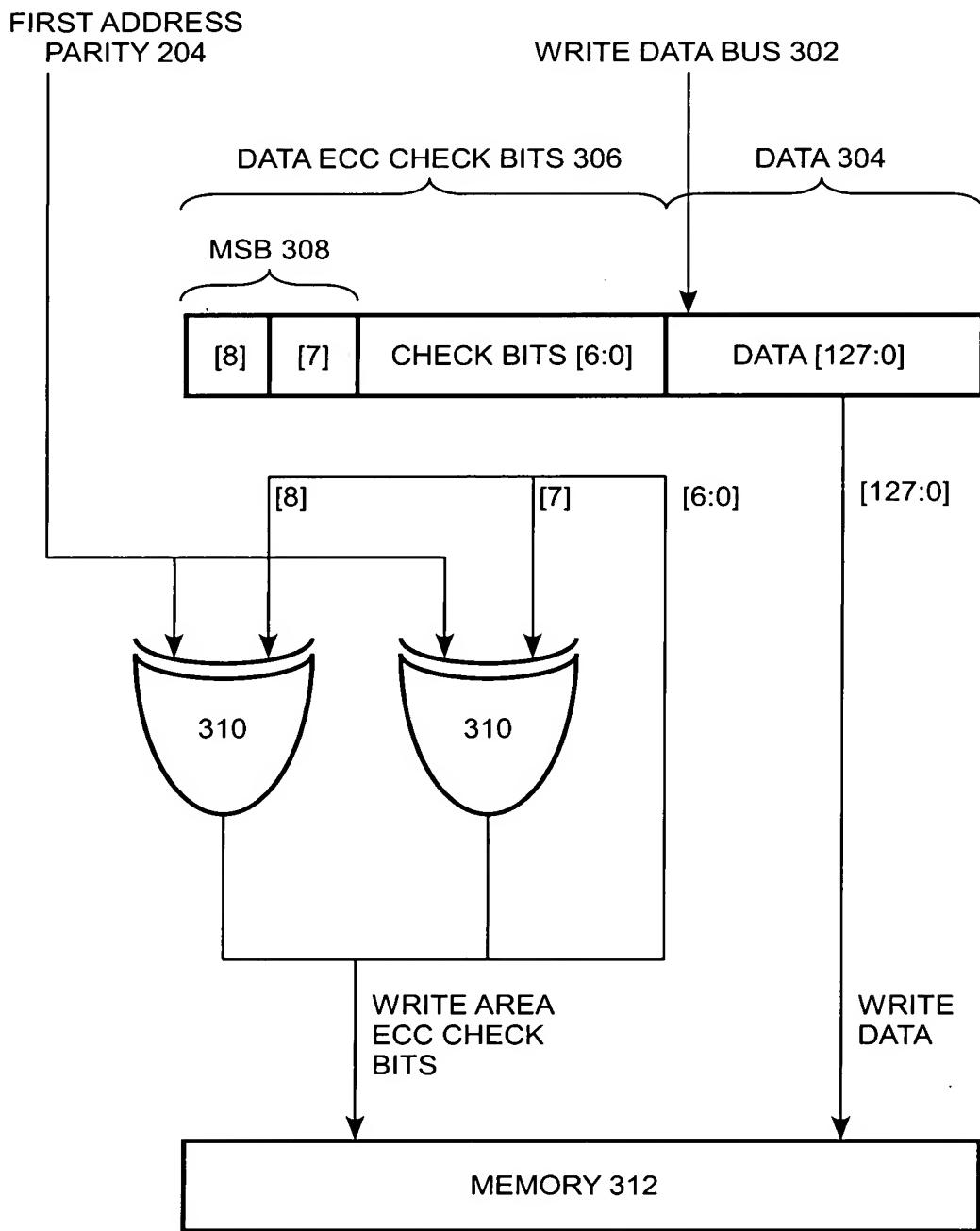


FIG. 4

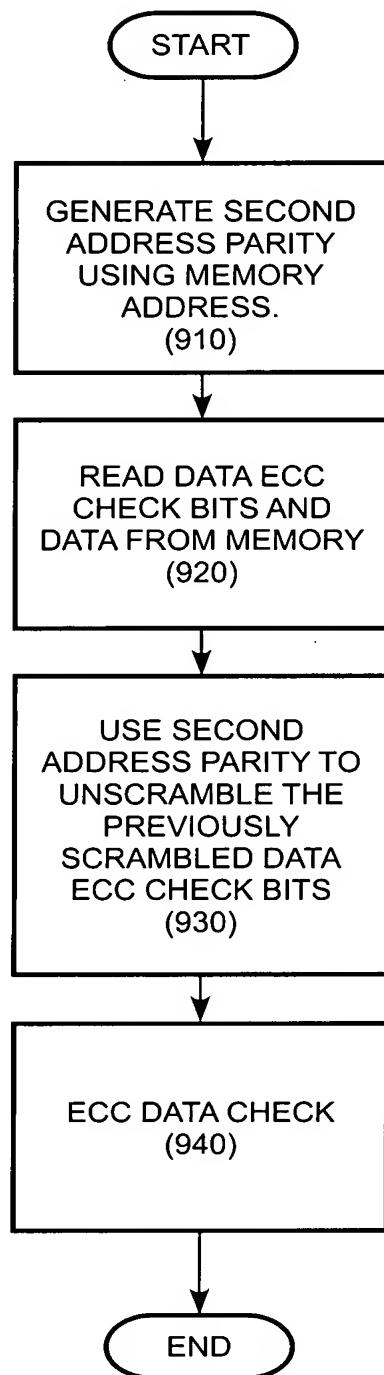


FIG. 5

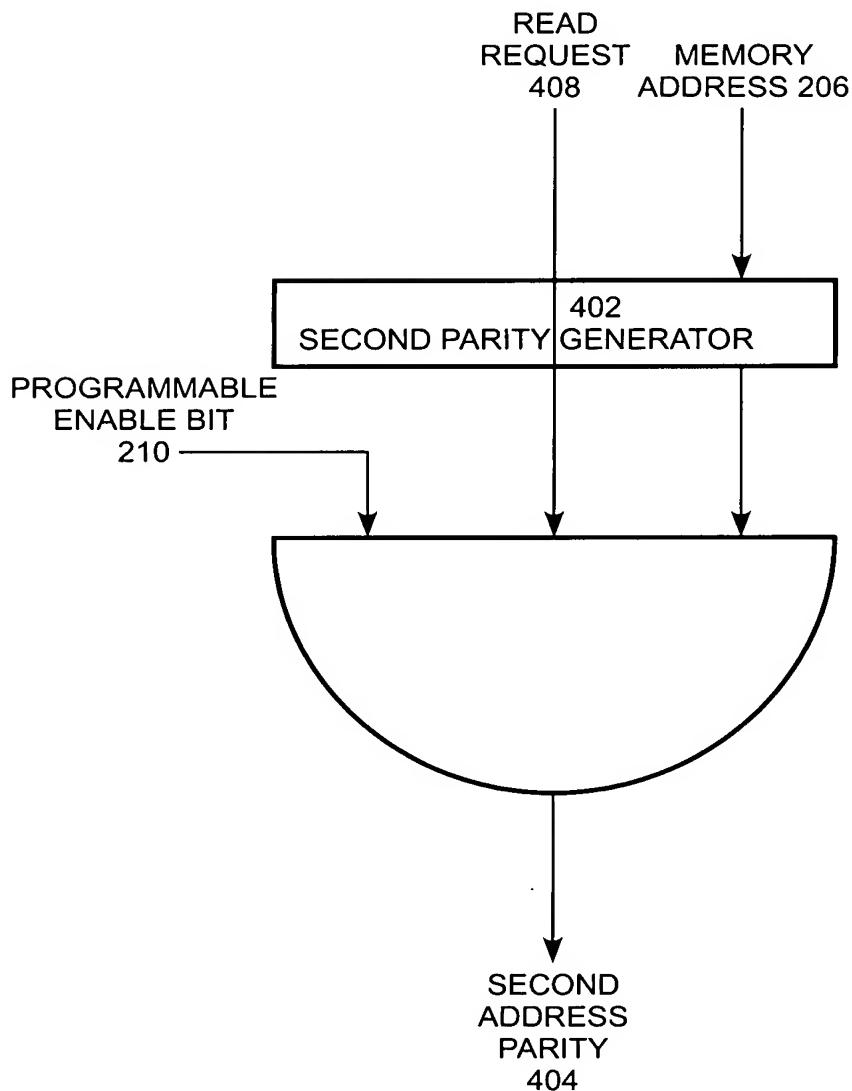


FIG. 6

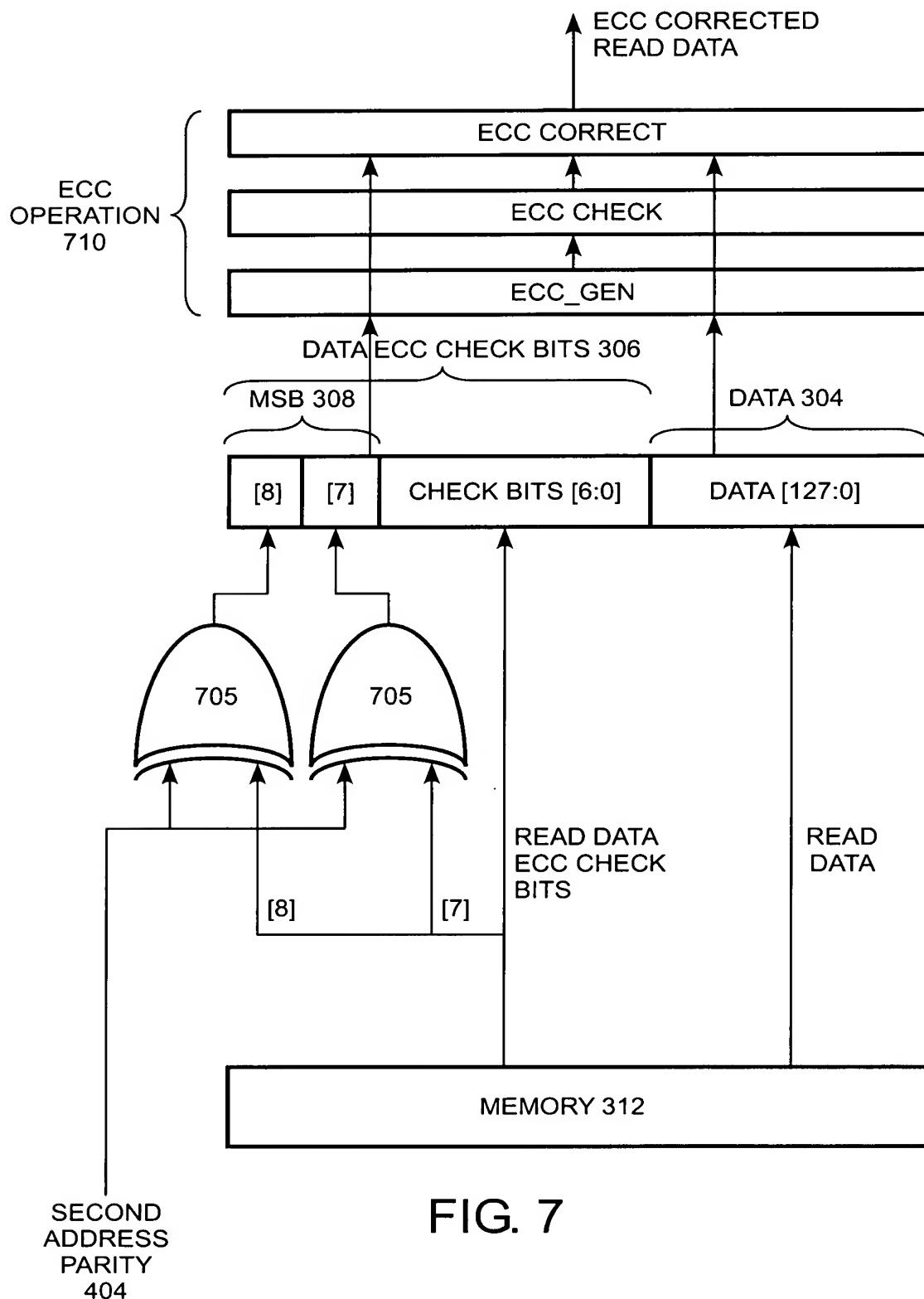
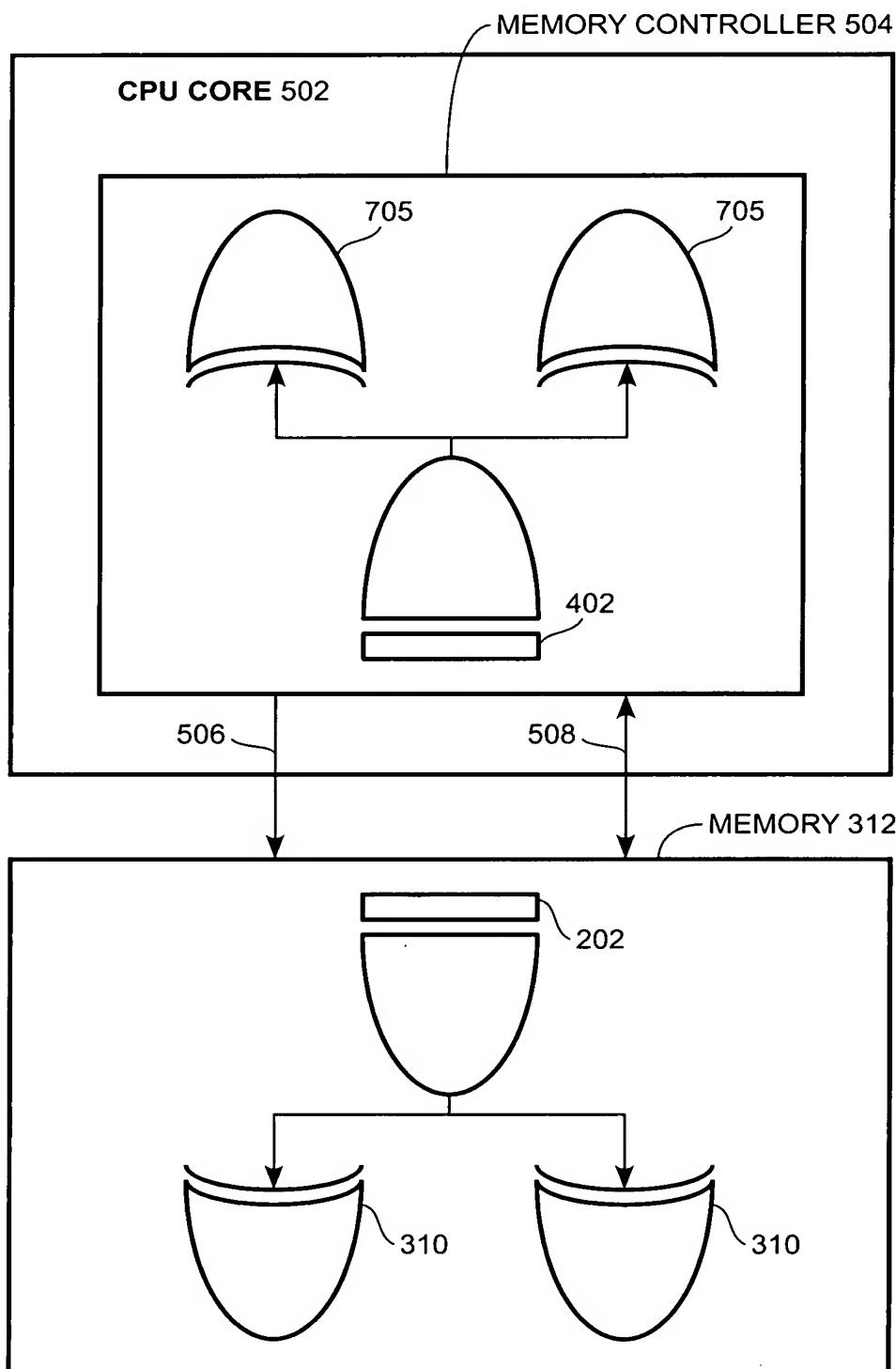


FIG. 7



**FIG. 8**